

# CZT

## SPECIFICATION FOR APPROVAL

**CUSTOMER** \_\_\_\_\_

**CUSTOMER'S PART NO.** \_\_\_\_\_ **REV.** \_\_\_\_\_

**DESCRIPTION** \_\_\_\_\_ **RJ45 ICM (10/100 Base-T 1 Port)**

**CZT'S PART NO.** \_\_\_\_\_ **JDFU10275101** **REV.** **A**

**CZT OLD P/N** \_\_\_\_\_

**ISSUE NO.** \_\_\_\_\_ **15B-278**

**ISSUE DATE** \_\_\_\_\_ **SEP.16.2015**

☐ **APPROVED**

☐ **REJECTED:**

☐ **APPROVED UNDER  
THE CONDITIONS:**

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**Note:**

- 1. This specification is the proprietary of Yihua Communicated Connector Co., Ltd. and is not allowed to be revealed to third party without any written approval.**
- 2. This specification is valid within 1 year before approval, and for any updated specification information required, please contact with CZT's sales window before order releases.**

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CZT Part No.: JDFU10275101  
Part Name: RJ45 ICM (10/100 Base-T 1 Port)

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CZT Part No.: JDFU10275101  
Part Name: RJ45 ICM (10/100 Base-T 1 Port)

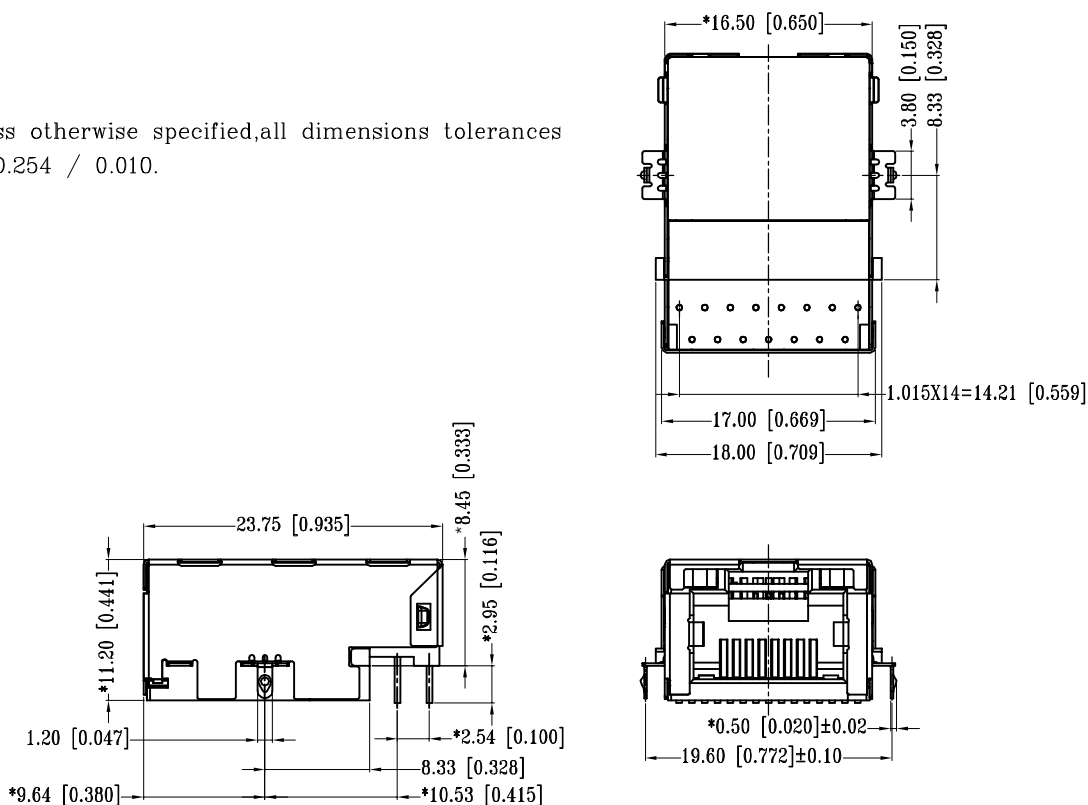
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### 4. Mechanical Dimension and Notes:

#### 4-1. Mechanical Dimension:

Unless otherwise specified, all dimensions tolerances is  $\pm 0.254$  /  $0.010$ .



#### 4-2. Notes:

- CONNECTOR MATERIAL:  
HOUSING: LCP BLACK UL94 V-0  
INSERTER: LCP BLACK UL94 V-0  
SHIELD: Brass  
SHIELD PLATING: NICKEL  
CONTACT: COOPER ALLOY  
CONTACT PLATING: SELECTIVE GOLD, 6 MICRO-INCHS MIN IN CONTACT AREA
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED SEE ELECTRICAL DRAWING FOR OMITTED PINS
- RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68.
- THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS  $260^{\circ}$  C MAX, 10 SECS MAX
- OPERATING TEMPERATURE  $T = -40^{\circ}$  C TO  $+85^{\circ}$  C.
- STORAGE TEMPERATURE  $T = -40^{\circ}$  C TO  $+85^{\circ}$  C.
- ALL CRITICAL DIMENSIONS WITH "★"

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Forward V(max)	TYP
GREEN	565nm	2.4V	2.2V
YELLOW	590nm	2.5V	2.1V

\*WITH A FORWARD CURRENT OF 20mA

Drawn by

Designed by

Approved by

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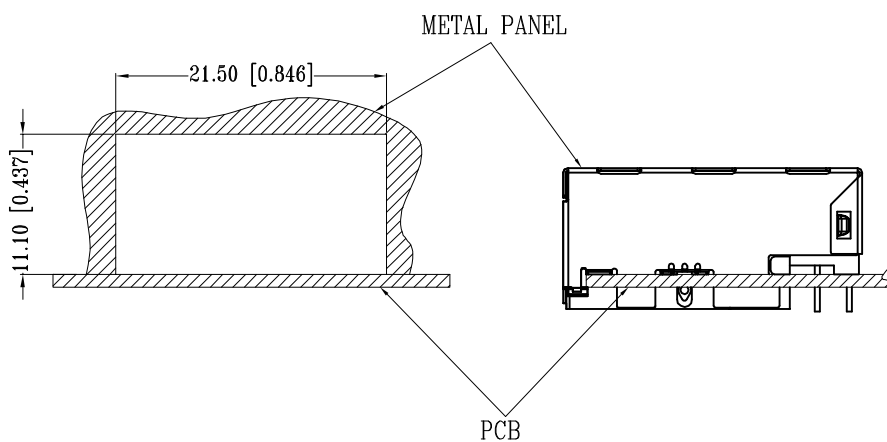
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### 5. Label and Recommended PWB Layout and Suggested Panel Opening:

#### 5-1. Suggested Panel Opening:



UNIT: mm / inch  
Tolerances:  $\pm 0.10$  / 0.004

REFERENCE ONLY

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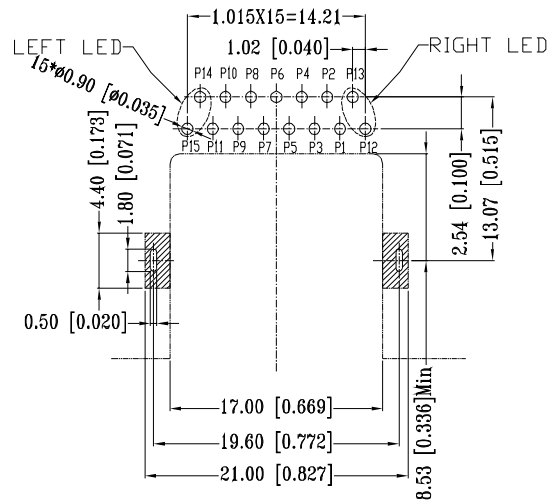
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### 5-2. Recommended PWB Layout (Component Side View):



UNIT: mm / inch  
Tolerances:  $\pm 0.10$  / 0.004

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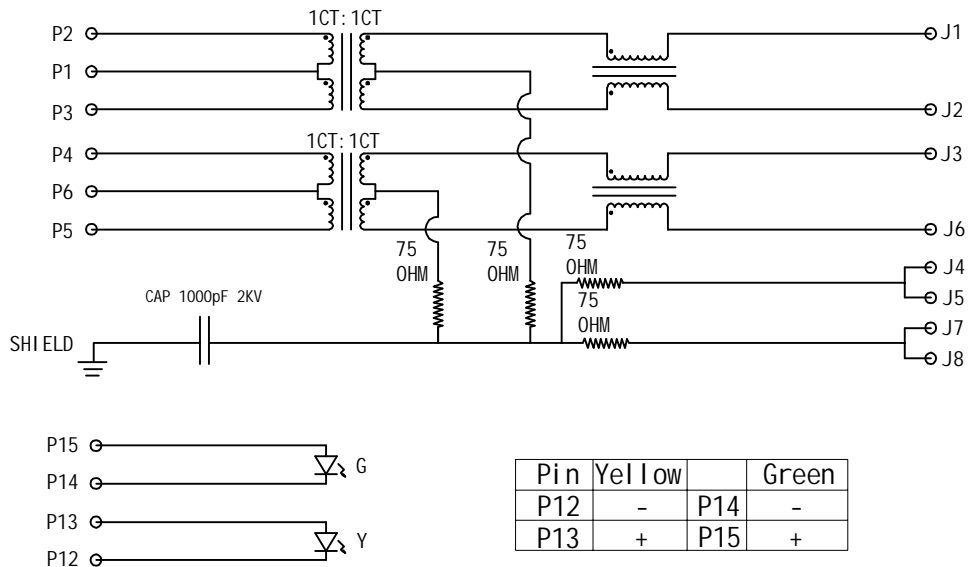
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### 6. Circuit Schematic and Electronical Specifications:

#### 6-1. Circuit Schematic:



#### 6-2. Electronical Specifications:

- 1.0 Turn Ratio @100KHz:  $(P2 \sim P3):(J1 \sim J2) = 1:1 \pm 5\%$   
 $(P4 \sim P5):(J3 \sim J6) = 1:1 \pm 5\%$
- 2.0 Primary Inductance: 350uH MIN @100KHZ, 0.1V 8mA DC BIAS
- 3.0 DC Resistance:  $(J1 \sim J2):(J3 \sim J6) = 1.2 \text{ OHMS MAX}$
- 4.0 Insertion Loss: 1-100MHz -1.2dB MAX
- 5.0 Return Loss: 1-30MHz -16dB MIN  
30-60MHz -12dB MIN  
60-80MHz -10dB MIN
- 6.0 CROSS TALK: 1-100MHz -30dB MIN
- 7.0 COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN
- 8.0 Isolation: PHY Side to Line Side: 1500VAC or 2250VDC